

## AMENDMENTS

### In the Claims:

Please amend claims 1, 3, 5, 7-14, 17, 19, 21, 24, 26, 28-154, 158-161, 164, 171, 172, 174-178, 181-185, 188-189, 191-195, 198-202, 205, 206, 208-212, 215-219, 222-226, 229-233, 236-240, 243, 244, 246-250, 253-257, 260, 261, 263-267, 270-274, 277, 278, 280-284 and 287-291. Pursuant to the revised format for amendments, the status of all of the claims is set forth below.

---

1. (Currently Amended) A memory address space extension device comprising:

B<sub>1</sub> a definition table defining an access right of a task ID (identification) with respect to a data area;

a task ID register for storing therein the task ID of a task during execution;

an extraction means for extracting a data area ID from a CPU (central processing unit) address inputted previously;

a judgement means for judging as to whether or not it is capable of being permitted that the task indicated by the task ID accesses the data area indicated by the data area ID, the judgement being made with reference to said definition table, and said judgement means judges whether or not said task is permitted access according to ~~any one of said task ID and an interruption processing number and/or a data area ID indicating the data area requested by said task.~~

2. (Previously Presented) A memory address space extension device as claimed in claim 1, further comprising an address operational means for converting said CPU

address into an extended address, and a memory having the extended address converted previously, and when said judgement means judges that the task during execution accesses the data area indicated by the data area ID, said address operational means executes said converting of said CPU address.

131 3. (Currently Amended) A memory address space extension device as claimed in claim 1, wherein[,] said definition table defines whether access is permissible or impermissible with respect to a read and / or write operation to said data area.

4. (Previously Presented) A memory address space extension device as claimed in claim 1, further comprising a preparation means for preparing said definition table.

5. (Currently Amended) A storage medium for storing therein a program in order to execute the processing of:

a storing processing for storing a task ID of a task during execution;

an extraction processing for extracting a data area ID from a CPU address inputted previously; and

a processing for judging as to whether or not the task indicated by the task ID accesses the data area indicated by the data area ID, the judgement being made with reference to said definition table, and said processing for judging judges whether or not said task is permitted access according to ~~any one of said task ID and an interruption processing number and/or a data area ID indicating the data area requested by said task.~~

6. (Previously Presented) A storage medium for storing therein a program as claimed in claim 5, wherein said program executes the processing of an address operational processing for converting said CPU address into an extended address when a judgement result according to said judging processing is that the task is permitted to access the data during execution.

7. (Currently Amended) A memory address space extension device comprising:  
  
a definition table for defining an access right of a task with respect to a data area;  
  
a definition table forming means for forming said definition table; and

31 an access right judgement means for judging, according to said definition table, whether or not the access right to the data area requested by the task should be provided with respect to the task, and said access right judgement means judges whether or not said task is permitted access according to ~~any one of a task ID and an interruption processing number and/or a data area ID indicating the data area requested by said task.~~

8. (Currently Amended) A memory address space extension device as claimed in claim 7, wherein said definition table defines whether or not said task possesses an access right for accessing a prescribed data area ~~in every~~ for any task ID associated with the task.

9. (Currently Amended) A memory address space extension device as claimed in claim 7, wherein said definition table defines whether or not said task possesses an access right for accessing a prescribed data area ~~in every~~ for any task ID associated with the task and / or in every interruption processing number ~~given to interruption processing.~~

10. (Currently Amended) A memory address space extension device as claimed in claim 7, wherein said access right judgement means judges whether it causes the access right to the data-area requested by said task to be given according to ~~said~~ a task ID, and ~~the~~ a data area ID indicating the data area requested by said task.

11. (Currently Amended) A memory address space extension device as claimed in claim 8, wherein said access right judgement means judges whether to permit access to the data area requested by said task according to said task ID, and ~~the~~ a data area ID indicating the data area requested by said task.

12. (Currently Amended) A memory address space extension device as claimed in claim 7, wherein said access right judgement means judges whether to permit access to the data area requested by said task according to ~~any one of said task ID~~ and said interruption processing number, and according to the data area ID indicating the data area requested by said task.

13. (Currently Amended) A memory address space extension device as claimed in claim 9, wherein said access right judgement means judges whether to permit access to the data area requested by said task according to ~~any one of said task ID~~ and said interruption processing number, and according to the data area ID indicating the data area requested by said task.

14. (Currently Amended) A memory address space extension device as claimed in claim 7, wherein said definition table defines whether or not access is permitted with respect to ~~said~~ a prescribed data area in every said task.

15. (Previously Presented) A memory address space extension device as claimed in claim 8, wherein said definition table defines whether or not access is permitted with respect to said prescribed data area in every said task.

16. (Previously Presented) A memory address space extension device as claimed in claim 9, wherein said definition table defines whether or not access is permitted with respect to said prescribed data area in every said task.

17. (Currently Amended) A memory address space extension device as claimed in claim 10, wherein said definition table defines whether or not access is permitted with respect to ~~said~~ a prescribed data area in every said task.

18. (Previously Presented) A memory address space extension device as claimed in claim 11, wherein said definition table defines whether or not access is permitted with respect to said prescribed data area in every said task.

19. (Currently Amended) A memory address space extension device as claimed in claim 12, wherein said definition table defines whether or not access is permitted with respect to ~~said~~ a prescribed data area in every said task.

20. (Previously Presented) A memory address space extension device as claimed in claim 13, wherein said definition table defines whether or not access is permitted with respect to said prescribed data area in every said task.

21. (Currently Amended) A memory address space extension device as claimed in claim 7, wherein said definition table defines any one of:

- (1) causing only writing of the data to be permitted;
- (2) causing reading of the data to be permitted;
- (3) causing writing of the data and reading of the data to be permitted; and
- (4) causing no-access to be permitted,

with respect to ~~said~~ a prescribed data area in every said task.

22. (Previously Presented) A memory address space extension device as claimed in claim 8, wherein said definition table defines any one of:

- (1) causing only writing of the data to be permitted;
- (2) causing reading of the data to be permitted;
- (3) causing writing of the data and reading of the data to be permitted; and
- (4) causing no-access to be permitted,

with respect to said prescribed data area in every said task.

23. (Previously Presented) A memory address space extension device as claimed in claim 9, wherein said definition table defines any one of:

- 31
- (1) causing only writing of the data to be permitted;
  - (2) causing reading of the data to be permitted;
  - (3) causing writing of the data and reading of the data to be permitted; and
  - (4) causing no-access to be permitted,

with respect to said prescribed data area in every said task.

24. (Currently Amended) A memory address space extension device as claimed in claim 10, wherein said definition table defines any one of:

- (1) causing only writing of the data to be permitted;
- (2) causing reading of the data to be permitted;
- (3) causing writing of the data and reading of the data to be permitted; and
- (4) causing no-access to be permitted,

with respect to ~~said~~ a prescribed data area in every said task.

25. (Previously Presented) A memory address space extension device as claimed in claim 11, wherein said definition table defines any one of:

- (1) causing only writing of the data to be permitted;
- (2) causing reading of the data to be permitted;
- (3) causing writing of the data and reading of the data to be permitted; and

(4) causing no-access to be permitted,

with respect to said prescribed data area in every said task.

131 26. (Currently Amended) A memory address space extension device as claimed in claim 12, wherein said definition table defines any one of:

(1) causing only writing of the data to be permitted;

(2) causing reading of the data to be permitted;

(3) causing writing of the data and reading of the data to be permitted; and

(4) causing no-access to be permitted,

with respect to ~~said~~ a prescribed data area in every said task.

27. (Previously Presented) A memory address space extension device as claimed in claim 13, wherein said definition table defines any one of:

(1) causing only writing of the data to be permitted;

(2) causing reading of the data to be permitted;

(3) causing writing of the data and reading of the data to be permitted; and

(4) causing no-access to be permitted,

with respect to said prescribed data area in every said task.

28. (Currently Amended) A memory address space extension device as claimed in claim 9, further comprising an interruption number generation means for generating the

the interruption processing number with respect to interruption processing according to hardware.

13 ( 29. (Currently Amended) A memory address space extension device as claimed in claim 12, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

30. (Currently Amended) A memory address space extension device as claimed in claim 13, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

31. (Currently Amended) A memory address space extension device as claimed in claim 14, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

32. (Currently Amended) A memory address space extension device as claimed in claim 15, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

33. (Currently Amended) A memory address space extension device as claimed in claim 16, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

34. (Currently Amended) A memory address space extension device as claimed in claim 17, further comprising an interruption number generation means for generating



the interruption processing number with respect to interruption processing according to hardware.

35. (Currently Amended) A memory address space extension device as claimed in claim 18, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

36. (Currently Amended) A memory address space extension device as claimed in claim 19, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

37. (Currently Amended) A memory address space extension device as claimed in claim 20, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

38. (Currently Amended) A memory address space extension device as claimed in claim 21, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

39. (Currently Amended) A memory address space extension device as claimed in claim 22, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

40. (Currently Amended) A memory address space extension device as claimed in claim 23, further comprising an interruption number generation means for generating

the interruption processing number with respect to interruption processing according to hardware.

41. (Currently Amended) A memory address space extension device as claimed in claim 24, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

42. (Currently Amended) A memory address space extension device as claimed in claim 25, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

43. (Currently Amended) A memory address space extension device as claimed in claim 26, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

44. (Currently Amended) A memory address space extension device as claimed in claim 27, further comprising an interruption number generation means for generating the interruption processing number with respect to interruption processing according to hardware.

45. (Currently Amended) A memory address space extension device as claimed in claim 9, further comprising an interruption number decoding means for decoding [[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

46. (Currently Amended) A memory address space extension device as claimed in claim 12, further comprising an interruption number decoding means for decoding

[[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

131 47. (Currently Amended) A memory address space extension device as claimed in claim 13, further comprising an interruption number decoding means for decoding [[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

48. (Currently Amended) A memory address space extension device as claimed in claim 14, further comprising an interruption number decoding means for decoding [[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

49. (Currently Amended) A memory address space extension device as claimed in claim 15, further comprising an interruption number decoding means for decoding [[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

50. (Currently Amended) A memory address space extension device as claimed in claim 16, further comprising an interruption number decoding means for decoding [[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

51. (Currently Amended) A memory address space extension device as claimed in claim 17, further comprising an interruption number decoding means for decoding [[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

52. (Currently Amended) A memory address space extension device as claimed in claim 18, further comprising an interruption number decoding means for decoding

[[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

53. (Currently Amended) A memory address space extension device as claimed in claim 19, further comprising an interruption number decoding means for decoding  
B( [[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

54. (Currently Amended) A memory address space extension device as claimed in claim 20, further comprising an interruption number decoding means for decoding  
[[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

55. (Currently Amended) A memory address space extension device as claimed in claim 21, further comprising an interruption number decoding means for decoding  
[[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

56. (Currently Amended) A memory address space extension device as claimed in claim 22, further comprising an interruption number decoding means for decoding  
[[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

57. (Currently Amended) A memory address space extension device as claimed in claim 23, further comprising an interruption number decoding means for decoding  
[[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

58. (Currently Amended) A memory address space extension device as claimed in claim 24, further comprising an interruption number decoding means for decoding

[[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

B, 59. (Currently Amended) A memory address space extension device as claimed in claim 25, further comprising an interruption number decoding means for decoding [[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

60. (Currently Amended) A memory address space extension device as claimed in claim 26, further comprising an interruption number decoding means for decoding [[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

61. (Currently Amended) A memory address space extension device as claimed in claim 27, further comprising an interruption number decoding means for decoding [[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

62. (Currently Amended) A memory address space extension device as claimed in claim 28, further comprising an interruption number decoding means for decoding [[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

63. (Currently Amended) A memory address space extension device as claimed in claim 29, further comprising an interruption number decoding means for decoding [[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

64. (Currently Amended) A memory address space extension device as claimed in claim 30, further comprising an interruption number decoding means for decoding

[[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

65. (Currently Amended) A memory address space extension device as claimed in claim 31, further comprising an interruption number decoding means for decoding  
B1 [[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

66. (Currently Amended) A memory address space extension device as claimed in claim 32, further comprising an interruption number decoding means for decoding  
[[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

67. (Currently Amended) A memory address space extension device as claimed in claim 33, further comprising an interruption number decoding means for decoding  
[[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

68. (Currently Amended) A memory address space extension device as claimed in claim 34, further comprising an interruption number decoding means for decoding  
[[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

69. (Currently Amended) A memory address space extension device as claimed in claim 35, further comprising an interruption number decoding means for decoding  
[[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

70. (Currently Amended) A memory address space extension device as claimed in claim 36, further comprising an interruption number decoding means for decoding

[[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

71. (Currently Amended) A memory address space extension device as claimed in claim 37, further comprising an interruption number decoding means for decoding [[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

72. (Currently Amended) A memory address space extension device as claimed in claim 38, further comprising an interruption number decoding means for decoding [[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

73. (Currently Amended) A memory address space extension device as claimed in claim 39, further comprising an interruption number decoding means for decoding [[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

74. (Currently Amended) A memory address space extension device as claimed in claim 40, further comprising an interruption number decoding means for decoding [[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

75. (Currently Amended) A memory address space extension device as claimed in claim 41, further comprising an interruption number decoding means for decoding [[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

76. (Currently Amended) A memory address space extension device as claimed in claim 42, further comprising an interruption number decoding means for decoding

[[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

31 77. (Currently Amended) A memory address space extension device as claimed in claim 43, further comprising an interruption number decoding means for decoding [[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

78. (Currently Amended) A memory address space extension device as claimed in claim 44, further comprising an interruption number decoding means for decoding [[an]] the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

79. (Currently Amended) A memory address space extension device as claimed in claim 7, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

80. (Currently Amended) A memory address space extension device as claimed in claim 8, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

81. (Currently Amended) A memory address space extension device as claimed in claim 9, further comprising an address operational means for converting the CPU



address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

βi 82. (Currently Amended) A memory address space extension device as claimed in claim 10, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

83. (Currently Amended) A memory address space extension device as claimed in claim 11, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

84. (Currently Amended) A memory address space extension device as claimed in claim 12, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

85. (Currently Amended) A memory address space extension device as claimed in claim 13, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

B1  
86. (Currently Amended) A memory address space extension device as claimed in claim 14, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

87. (Currently Amended) A memory address space extension device as claimed in claim 15, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

88. (Currently Amended) A memory address space extension device as claimed in claim 16, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area

according to said access right judgement means, before said memory with extended address supports said address operational means.

B/ 89. (Currently Amended) A memory address space extension device as claimed in claim 17, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

90. (Currently Amended) A memory address space extension device as claimed in claim 18, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

91. (Currently Amended) A memory address space extension device as claimed in claim 19, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

92. (Currently Amended) A memory address space extension device as claimed in claim 20, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted

into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

B<sub>1</sub> 93. (Currently Amended) A memory address space extension device as claimed in claim 21, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

94. (Currently Amended) A memory address space extension device as claimed in claim 22, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

95. (Currently Amended) A memory address space extension device as claimed in claim 23, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

96. (Currently Amended) A memory address space extension device as claimed in claim 24, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted

previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

B<sub>1</sub>  
97. (Currently Amended) A memory address space extension device as claimed in claim 25, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

98. (Currently Amended) A memory address space extension device as claimed in claim 26, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

99. (Currently Amended) A memory address space extension device as claimed in claim 27, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

100. (Currently Amended) A memory address space extension device as claimed in claim 28, further comprising an address operational means for converting the

CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

B<sub>1</sub> 101. (Currently Amended) A memory address space extension device as claimed in claim 29, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

102. (Currently Amended) A memory address space extension device as claimed in claim 30, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

103. (Currently Amended) A memory address space extension device as claimed in claim 31, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

104. (Currently Amended) A memory address space extension device as claimed in claim 32, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

B<sub>1</sub>  
105. (Currently Amended) A memory address space extension device as claimed in claim 33, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

106. (Currently Amended) A memory address space extension device as claimed in claim 34, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

107. (Currently Amended) A memory address space extension device as claimed in claim 35, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data

area according to said access right judgement means, before said memory with extended address supports said address operational means.

B1 108. (Currently Amended) A memory address space extension device as claimed in claim 36, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

109. (Currently Amended) A memory address space extension device as claimed in claim 37, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

110. (Currently Amended) A memory address space extension device as claimed in claim 38, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

111. (Currently Amended) A memory address space extension device as claimed in claim 39, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be



converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

112. (Currently Amended) A memory address space extension device as claimed in claim 41, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

113. (Currently Amended) A memory address space extension device as claimed in claim 42, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

114. (Currently Amended) A memory address space extension device as claimed in claim 43, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

115. (Currently Amended) A memory address space extension device as claimed in claim 44, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address

converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

116. (Currently Amended) A memory address space extension device as claimed in claim 45, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

117. (Currently Amended) A memory address space extension device as claimed in claim 46, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

118. (Currently Amended) A memory address space extension device as claimed in claim 47, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

119. (Currently Amended) A memory address space extension device as claimed in claim 48, further comprising an address operational means for converting the

CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

B<sub>1</sub> 120. (Currently Amended) A memory address space extension device as claimed in claim 49, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

121. (Currently Amended) A memory address space extension device as claimed in claim 50, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

122. (Currently Amended) A memory address space extension device as claimed in claim 51, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

123. (Currently Amended) A memory address space extension device as claimed in claim 52, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

124. (Currently Amended) A memory address space extension device as claimed in claim 53, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

125. (Currently Amended) A memory address space extension device as claimed in claim 54, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

126. (Currently Amended) A memory address space extension device as claimed in claim 55, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area

according to said access right judgement means, before said memory with extended address supports said address operational means.

127. (Currently Amended) A memory address space extension device as claimed in claim 56, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

128. (Currently Amended) A memory address space extension device as claimed in claim 58, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

129. (Currently Amended) A memory address space extension device as claimed in claim 59, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

130. (Currently Amended) A memory address space extension device as claimed in claim 60, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be

converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

131. (Currently Amended) A memory address space extension device as claimed in claim 61, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

132. (Currently Amended) A memory address space extension device as claimed in claim 62, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

133. (Currently Amended) A memory address space extension device as claimed in claim 63, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

134. (Currently Amended) A memory address space extension device as claimed in claim 64, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address

converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

B1 135. (Currently Amended) A memory address space extension device as claimed in claim 65, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

136. (Currently Amended) A memory address space extension device as claimed in claim 66, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

137. (Currently Amended) A memory address space extension device as claimed in claim 67, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

138. (Currently Amended) A memory address space extension device as claimed in claim 68, further comprising an address operational means for converting the

CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

B<sub>1</sub> 139. (Currently Amended) A memory address space extension device as claimed in claim 69, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

140. (Currently Amended) A memory address space extension device as claimed in claim 70, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

141. (Currently Amended) A memory address space extension device as claimed in claim 71, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.



B<sub>1</sub>

142. (Currently Amended) A memory address space extension device as claimed in claim 72, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

143. (Currently Amended) A memory address space extension device as claimed in claim 73, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

144. (Currently Amended) A memory address space extension device as claimed in claim 74, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

145. (Currently Amended) A memory address space extension device as claimed in claim 75, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data

area according to said access right judgement means, before said memory with extended address supports said address operational means.

146. (Currently Amended) A memory address space extension device as claimed in claim 76, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

147. (Currently Amended) A memory address space extension device as claimed in claim 77, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

148. (Currently Amended) A memory address space extension device as claimed in claim 78, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

149. (Currently Amended) A memory address space extension device as claimed in claim 79, further comprising an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be

converted into the extended address when said task is permitted [[to]] access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

150. (Currently Amended) A storage medium storing therein a program in order to execute the processing of:

B<sub>1</sub> a definition table preparation processing for preparing a definition table;

a reference processing for said definition table which defines an access right of a task with respect to a data area; and

an access right judgement processing for judging whether or not said task is permitted access for the data area requested by said task according to said definition table, and said access right judgement processing judges whether or not said task is permitted access according to ~~any one of a task ID and~~ an interruption processing number ~~and/or a data area ID indicating the data area requested by said task.~~

151. (Currently Amended) A storage medium storing therein a program as claimed in claim 150, wherein said reference processing for said definition table causes said definition table to define whether or not said task possesses the access right for accessing a prescribed data area to be seen ~~in every~~ for any task ID associated with said task.

152. (Currently Amended) A storage medium storing therein a program as claimed in claim 150, wherein said reference processing for said definition table causes the definition table defining whether or not said task possesses the access right for accessing a prescribed data area to be seen ~~in every~~ for any task ID given to said task and / or in every interruption number ~~given to the interruption processing.~~

153. (Currently Amended) A storage medium storing therein a program as claimed in claim 150, wherein said access right judgement processing judges whether or

not said task possesses the access right for accessing a prescribed data area according to ~~said~~  
a task ID and / or ~~said~~ a data area ID indicating the data area requested by said task.

154. (Currently Amended) A storage medium storing therein a program as claimed in claim 151, wherein said access right judgement processing judges whether or not said task possesses the access right for accessing ~~[[a]]~~ said prescribed data area according to said task ID and / or ~~said~~ a data area ID indicating the data area requested by said task.

155. (Cancelled)

156. (Cancelled)

157. (Previously Presented) A storage medium storing therein a program as claimed in claim 150, wherein said reference processing for said definition table causes said definition table to define whether or not said task possesses the access right for accessing a prescribed data area to be seen in every said task.

158. (Currently Amended) A storage medium storing therein a program as claimed in claim 151, wherein said reference processing for said definition table causes said definition table to define whether or not said task possesses the access right for accessing ~~[[a]]~~ said prescribed data area to be seen in every said task.

159. (Currently Amended) A storage medium storing therein a program as claimed in claim 152, wherein said reference processing for said definition table causes said definition table to define whether or not said task possesses the access right for accessing ~~[[a]]~~ said prescribed data area to be seen in every said task.

160. (Currently Amended) A storage medium storing therein a program as claimed in claim 153, wherein said reference processing for said definition table causes said definition table to define whether or not said task possesses the access right for accessing ~~[[a]]~~ said prescribed data area to be seen in every said task.

161. (Currently Amended) A storage medium storing therein a program as claimed in claim 154, wherein said reference processing for said definition table causes said definition table to define whether or not said task possesses the access right for accessing said prescribed data area to be seen in every said task.

162. (Cancelled)

163. (Cancelled)

164. (Currently Amended) A storage medium storing therein a program as claimed in claim 150, wherein said reference processing for said definition table causes any one of:

- (1) allowing only writing of the data to be permitted;
- (2) allowing reading of the data to be permitted;
- (3) allowing writing of the data and reading of the data to be permitted; and
- (4) allowing no-access to be permitted,

to be seen with respect to ~~said~~ a prescribed data area in every said task.

165. (Previously Presented) A storage medium storing therein a program as claimed in claim 151, wherein said reference processing for said definition table causes any one of:

- (1) allowing only writing of the data to be permitted;
- (2) allowing reading of the data to be permitted;
- (3) allowing writing of the data and reading of the data to be permitted; and

(4) allowing no-access to be permitted,

to be seen with respect to said prescribed data area in every said task.

166. (Previously Presented) A storage medium storing therein a program as claimed in claim 152, wherein said reference processing for said definition table causes any one of:

131 (1) allowing only writing of the data to be permitted;

(2) allowing reading of the data to be permitted;

(3) allowing writing of the data and reading of the data to be permitted; and

(4) allowing no-access to be permitted,

to be seen with respect to said prescribed data area in every said task.

167. (Previously Presented) A storage medium storing therein a program as claimed in claim 153, wherein said reference processing for said definition table causes any one of:

(1) allowing only writing of the data to be permitted;

(2) allowing reading of the data to be permitted;

(3) allowing writing of the data and reading of the data to be permitted; and

(4) allowing no-access to be permitted,

to be seen with respect to said prescribed data area in every said task.

168. (Previously Presented) A storage medium storing therein a program as claimed in claim 154, wherein said reference processing for said definition table causes any one of:

- 131
- (1) allowing only writing of the data to be permitted;
  - (2) allowing reading of the data to be permitted;
  - (3) allowing writing of the data and reading of the data to be permitted; and
  - (4) allowing no-access to be permitted,

to be seen with respect to said prescribed data area in every said task.

169. (Cancelled)

170. (Cancelled)

171. (Currently Amended) A storage medium storing therein a program as claimed in claim 152, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

172. (Currently Amended) A storage medium storing therein a program as claimed in claim 150, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

173. (Cancelled)

174. (Currently Amended) A storage medium storing therein a program as claimed in claim 157, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

175. (Currently Amended) A storage medium storing therein a program as claimed in claim 158, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

176. (Currently Amended) A storage medium storing therein a program as claimed in claim 159, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

177. (Currently Amended) A storage medium storing therein a program as claimed in claim 160, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

178. (Currently Amended) A storage medium storing therein a program as claimed in claim 161, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

179. (Cancelled)

180. (Cancelled)

181. (Currently Amended) A storage medium storing therein a program as claimed in claim 164, wherein said program further executes interruption number



generation processing for generating the interruption processing number to interruption processing according to hardware.

182. (Currently Amended) A storage medium storing therein a program as claimed in claim 165, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

B1 183. (Currently Amended) A storage medium storing therein a program as claimed in claim 166, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

184. (Currently Amended) A storage medium storing therein a program as claimed in claim 167, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

185. (Currently Amended) A storage medium storing therein a program as claimed in claim 168, wherein said program further executes interruption number generation processing for generating the interruption processing number to interruption processing according to hardware.

186. (Cancelled)

187. (Cancelled)

188. (Currently Amended) A storage medium storing therein a program as claimed in claim 152, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

189. (Currently Amended) A storage medium storing therein a program as claimed in claim 150, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

190. (Cancelled)

191. (Currently Amended) A storage medium storing therein a program as claimed in claim 157, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

192. (Currently Amended) A storage medium storing therein a program as claimed in claim 158, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

193. (Currently Amended) A storage medium storing therein a program as claimed in claim 159, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

194. (Currently Amended) A storage medium storing therein a program as claimed in claim 160, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

195. (Currently Amended) A storage medium storing therein a program as claimed in claim 161, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

196. (Cancelled)

197. (Cancelled)

198. (Currently Amended) A storage medium storing therein a program as claimed in claim 164, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

199. (Currently Amended) A storage medium storing therein a program as claimed in claim 165, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

200. (Currently Amended) A storage medium storing therein a program as claimed in claim 166, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

201. (Currently Amended) A storage medium storing therein a program as claimed in claim 167, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

202. (Currently Amended) A storage medium storing therein a program as claimed in claim 168, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

203. (Cancelled)

204. (Cancelled)

205. (Currently Amended) A storage medium storing therein a program as claimed in claim 171, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

206. (Currently Amended) A storage medium storing therein a program as claimed in claim 172, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

131

207. (Cancelled)

208. (Currently Amended) A storage medium storing therein a program as claimed in claim 174, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

209. (Currently Amended) A storage medium storing therein a program as claimed in claim 175, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

210. (Currently Amended) A storage medium storing therein a program as claimed in claim 176, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

211. (Currently Amended) A storage medium storing therein a program as claimed in claim 177, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

212. (Currently Amended) A storage medium storing therein a program as claimed in claim 178, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

213. (Cancelled)

214. (Cancelled)

215. (Currently Amended) A storage medium storing therein a program as claimed in claim 181, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

216. (Currently Amended) A storage medium storing therein a program as claimed in claim 182, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

217. (Currently Amended) A storage medium storing therein a program as claimed in claim 183, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

218. (Currently Amended) A storage medium storing therein a program as claimed in claim 184, wherein said program further executes interruption number decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

219. (Currently Amended) A storage medium storing therein a program as claimed in claim 185, wherein said program further executes interruption number

decoding processing for decoding the interruption processing number from an interruption instruction with respect to said interruption instruction according to software.

220. (Cancelled)

221. (Cancelled)

222. (Currently Amended) A storage medium storing therein a program as claimed in claim 150, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

223. (Currently Amended) A storage medium storing therein a program as claimed in claim 151, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

224. (Currently Amended) A storage medium storing therein a program as claimed in claim 152, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address ~~to be~~ is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

225. (Currently Amended) A storage medium storing therein a program as claimed in claim 153, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is

converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

226. (Currently Amended) A storage medium storing therein a program as claimed in claim 154, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

B1

227. (Cancelled)

228. (Cancelled)

229. (Currently Amended) A storage medium storing therein a program as claimed in claim 157, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

230. (Currently Amended) A storage medium storing therein a program as claimed in claim 158, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

231. (Currently Amended) A storage medium storing therein a program as claimed in claim 159, wherein said program executes an address operational processing in

order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

232. (Currently Amended) A storage medium storing therein a program as claimed in claim 160, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

233. (Currently Amended) A storage medium storing therein a program as claimed in claim 161, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

234. (Cancelled)

235. (Cancelled)

236. (Currently Amended) A storage medium storing therein a program as claimed in claim 164, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.



237. (Currently Amended) A storage medium storing therein a program as claimed in claim 165, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

B1

238. (Currently Amended) A storage medium storing therein a program as claimed in claim 166, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

239. (Currently Amended) A storage medium storing therein a program as claimed in claim 167, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

240. (Currently Amended) A storage medium storing therein a program as claimed in claim 168, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

241. (Cancelled)

242. (Cancelled)

B1  
243. (Currently Amended) A storage medium storing therein a program as claimed in claim 171, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

244. (Currently Amended) A storage medium storing therein a program as claimed in claim 172, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

245. (Cancelled)

246. (Currently Amended) A storage medium storing therein a program as claimed in claim 174, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

247. (Currently Amended) A storage medium storing therein a program as claimed in claim 175, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

248. (Currently Amended) A storage medium storing therein a program as claimed in claim 176, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

249. (Currently Amended) A storage medium storing therein a program as claimed in claim 177, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

250. (Currently Amended) A storage medium storing therein a program as claimed in claim 178, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

251. (Cancelled)

252. (Cancelled)

253. (Currently Amended) A storage medium storing therein a program as claimed in claim 181, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

254. (Currently Amended) A storage medium storing therein a program as claimed in claim 182, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

101 255. (Currently Amended) A storage medium storing therein a program as claimed in claim 183, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

256. (Currently Amended) A storage medium storing therein a program as claimed in claim 184, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

257. (Currently Amended) A storage medium storing therein a program as claimed in claim 185, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

258. (Cancelled)

259. (Cancelled)

260. (Currently Amended) A storage medium storing therein a program as claimed in claim 188, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

B1

261. (Currently Amended) A storage medium storing therein a program as claimed in claim 189, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

262. (Cancelled)

263. (Currently Amended) A storage medium storing therein a program as claimed in claim 191, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

264. (Currently Amended) A storage medium storing therein a program as claimed in claim 192, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

265. (Currently Amended) A storage medium storing therein a program as claimed in claim 193, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in~~ such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

266. (Currently Amended) A storage medium storing therein a program as claimed in claim 194, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

267. (Currently Amended) A storage medium storing therein a program as claimed in claim 195, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

268. (Cancelled)

269. (Cancelled)

270. (Currently Amended) A storage medium storing therein a program as claimed in claim 198, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

271. (Currently Amended) A storage medium storing therein a program as claimed in claim 199, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

β1 272. (Currently Amended) A storage medium storing therein a program as claimed in claim 200, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

273. (Currently Amended) A storage medium storing therein a program as claimed in claim 201, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

274. (Currently Amended) A storage medium storing therein a program as claimed in claim 202, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

275. (Cancelled)

276. (Cancelled)

277. (Currently Amended) A storage medium storing therein a program as claimed in claim 205, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

B1  
278. (Currently Amended) A storage medium storing therein a program as claimed in claim 206, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

279. (Cancelled)

280. (Currently Amended) A storage medium storing therein a program as claimed in claim 208, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

281. (Currently Amended) A storage medium storing therein a program as claimed in claim 209, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.



282. (Currently Amended) A storage medium storing therein a program as claimed in claim 210, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

β1 283. (Currently Amended) A storage medium storing therein a program as claimed in claim 211, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

284. (Currently Amended) A storage medium storing therein a program as claimed in claim 212, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

285. (Cancelled)

286. (Cancelled)

287. (Currently Amended) A storage medium storing therein a program as claimed in claim 215, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

288. (Currently Amended) A storage medium storing therein a program as claimed in claim 216, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

289. (Currently Amended) A storage medium storing therein a program as claimed in claim 217, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

290. (Currently Amended) A storage medium storing therein a program as claimed in claim 218, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

291. (Currently Amended) A storage medium storing therein a program as claimed in claim 219, wherein said program executes an address operational processing in order to convert a CPU address into an extended address such that said CPU address is converted into said extended address when it is judged that said task is permitted [[to]] access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

292. (Cancelled)

293. (Cancelled)